

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

**Group Art Unit: 2186** 

David Carroll Challener et al;

Examiner: unknown

-Serial Number: 10/734,960

Customer No. 25299

Filed: December 12, 2003

Title: Detecting Modifications made to Code Placed in Memory by the Post BIOS

## **SUBMISSION OF FORMAL DRAWINGS**

Mail Stop: Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby submit three (3) sheets of formal drawings for Figs.1 - 2.

Respectfully submitted,

By:

CARLOS MUNOZ-BUSTAMANTE

Registration No.51,349

(919) 254 2587

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